	Cod	de No: RT31041	3	SET - 1	
	III B. Tech I Semester Supplementary Examinations, May - 2016 PULSE AND DIGITAL CIRCUITS (Common to ECE and EIE)				
	Time: 3 hours		-	Max. Marks: 70	
 Note: 1. Question Paper consists of two parts (Part-A and Part-B) 2. Answering the question in Part-A is compulsory 3. Answer any THREE Questions from Part-B 					
PART –A					
1	a)	Describe about ringing circuit.		[3M]	
	b)	State clamping circuit theorem.		[4M]	
	c)	Explain piecewise linear characteristics of a d	iode.	[4M]	
	d)	What are the types of triggering? Distinguish	between them.	[4M]	
	e)	What are the applications of Time-base generation	ator?	[3M]	
	f)	What do you mean by sampling gate? Give th	e applications of sampling gate?	[4M]	
PART -B					
2	a)	Discuss about attenuators.		[4M]	
	b)	Analyze the low pass circuit for the exponenti	al inputs, with help of waveforms.	[8M]	
	c)	Discuss the application of an attenuator in a C	RO.	[4M]	
3	a)	Draw the basic circuit diagram of positive operation.			
	b)	Explain transfer characteristics of emitter equations.	coupled clipper and derive nece	essary [8M]	
4	a)	Draw the circuits of 3-input OR-gate using die (i) Positive logic, (ii) Negative logic and expla		[8M]	
	b)	Give the comparison of different logic familie	S.	[8M]	
5	a)	What are different types of multivibrators? E	xplain the stable state of a multivib	rator. [8M]	
	b)	Sketch the circuit diagram of Schmitt trigger a	nd explain its operation.	[8M]	
6	a)	With the help of neat circuit diagram and w base generator.	vaveforms explain transistor miller	time [8M]	
	b)	Discuss about the recovery time of a swe recovery time?	ep circuit. How do you achieve	short [8M]	
7	a) b)	Explain the process of synchronization of a sw Write notes on: i) Astable relaxation circuits ii) Monostable re	-	[8M] [8M]	
